A FLASH MCU SOLUTION

68HC908AZ60A

8-bit Microcontroller

TARGET APPLICATIONS

- Automotive applications
- Industrial controls
- Medical electronics
- Sensors/measurement devices

A highly integrated, high-performance microcontroller with integrated controller area network (CAN), the 68HC908AZ60A creates new opportunities for cost-effective product design. The 68HC908AZ60A uses the proven 68HC08 architecture and embedded FLASH memory for enhanced speed, power and functionality. Upwardly compatible with 68HC05 architecture, this microcontroller family reduces operating and programming costs through low-power usage and by eliminating the need for external serial EEPROM. Features include an analog-todigital converter (ADC), Motorola scalable controller area network (msCAN), synchronous serial peripheral interface (SPI), asynchronous serial communications interface (SCI) and keyboard interrupts (KBI).



FEATURES

HIGH-PERFORMANCE 68HC08 CPU CORE

- 8 MHz bus operation at 5V operation for 125 nsec minimum instruction cycle time
- Efficient instruction set including multiply and divide
- 16 flexible addressing modes including stack relative with 16-bit stack pointer
- Fully static low-voltage, low-power design with wait and stop modes

INTEGRATED SECOND GENERATION FLASH MEMORY

- In-application re-programmable
- Extremely fast programming, encoding 64 bytes in as fast as 2 msec
- FLASH programming across the 68HC08's full operating supply voltage with no extra programming voltage
- 10K write/erase cycles minimum over temperature
- · Flexible block protection and security

INTEGRATED EEPROM

• Object code compatible with the 68HC05

BENEFITS

- Easy to learn and use architecture
- C optimized architecture provides compact code
- Cost-effective programming changes and field software upgrades via in-application programmability and re-programmability
- Reduces production programming costs through ultra-fast programming
- Byte-writable for data as well as program memory
- Protects code from unauthorized reading and to guard against unintentional erasing/writing of user-programmable segments of code

 Fast, easy conversion from analog inputs like temperature, pressure and fluid levels

· Provides high performance using low-cost,

providing high performance (up to 32 MHz

low-frequency reference crystals

Reduces generated noise while still

internal clock)

to digital values for CPU processing

• Byte erasable

8-BIT ANALOG-TO-DIGITAL CONVERTER

- 15 channels
- Single conversion in 17 µsec

CLOCK GENERATION MODULE WITH PLL

- Programmable clock frequency in integer multiples of external crystal reference
- Crystal reference of 1 MHz to 16 MHz
- External clock option with or without PLL

EIGHT PROGRAMMABLE 16-BIT TIMER CHANNELS

- 125 nsec resolution at 8 MHz bus
- · Free-running counter or modulo up-counter
- Each channel independently programmable for input capture, output compare or unbuffered PWM
- Pairing timer channels provides a buffered PWM function





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PART NUMBER	DESCRIPTION	RESALE*				
EASY-TO-ORDER DEVELOPMENT TOOL KITS						
M68ICS08ASAZ	Programmer/in-circuit debug kit	\$295				
KITMMEVS08ASAZ	Cost-effective real-time in-circuit emulator kit	\$1450				
KITMMDS08ASAZ	High-performance real-time in-circuit emulator kit	\$3950				
INDIVIDUAL DEVI	ELOPMENT TOOL COM	IPONENTS				
M68MMDS0508	High-performance	\$2950				

M68MMDS0508	High-performance MMDS0508 emulator	\$2950
M68MMPFB0508	system Cost-effective MMEVS	\$395
IVIOOIVIIVIFFDUJUO	modular evaluation system	\$29D
M68EM08AS/AZ60A	Emulation module daughter board	\$495
M68CBL05C	Low-noise flex cable	\$120
X68TC08AX48FU64	64-Lead QFP target head adapter	\$250
M68TQS064SAG1	64-Lead TQ socket with guides	\$50
M68TQP064SA1	64-Lead TOPACK	\$70

APPLICATION NOTES

- AN1798/D CAN Bit Timing Requirements AN2093/D Creating Efficient C Code for the
- MC68HC08
- AN1219/D M68HC08 Integer Math Routines
- AN1218/D HC05 to HC08 Optimization
- MCI/06/D Noise Reduction MCU-Based Systems AN1259/D System Design and Layout Techniques for Noise Reduction in MCU-Based Systems AN1263/D Designing for Electromagnetic Compatibility with Single-Chip Microcontrollers
- AN1050/D Designing for Electromagnetic Compatibility (EMC) with HCMOS Microcontrollers
- AN1705/D Noise Reduction Techniques for

FEA	TURES

PERIODIC INTERRUPT TIMER

· Provides periodic interrupts

SERIAL COMMUNICATIONS INTERFACE

- UART asynchronous communications system
- · Flexible baud rate generator
- Double buffered transmit and receive
- Optional hardware parity checking and generation

SERIAL PERIPHERAL INTERFACE

- Full-duplex 3-wire synchronous transfers
- · Maximum master bit rate of 4 MHz for 8 MHz system clock

 Asynchronous communication between the MCU and a terminal, computer or a network of microcontrollers

BENEFITS

- · High-speed synchronous communication between multiple MCUs or between MCU and serial peripherals
- Cost-effective serial peripheral expansion to EEPROM, high precision A/D and D/A converters, real-time clocks, etc.

COMPUTER OPERATING PROPERLY WATCHDOG TIMER

· Provides system protection in the event of runaway code by resetting the MCU to a known state

LOW-VOLTAGE INHIBIT

- Improves reliability by resetting the MCU when voltage drops below trip point
- Integration reduces system cost

UP TO 51 BIDIRECTIONAL INPUT/OUTPUT (I/O) LINES

- 10 mA sink/source capability on all I/O pins
- Keyboard scan with selectable interrupts on five I/O pins
- High-current capable I/O allows direct drive of LED and other circuits to eliminate external drivers and reduce system costs
- Keyboard scan with programmable pullups eliminates external glue logic when interfacing to simple keypads

PACKAGE OPTIONS

PART NUMBER	PACKAGE	TEMPERATURE RANGE
MC908AZ60ACFU MC908AZ60AVFU MC908AZ60AMFU	64 QFP 64 QFP 64 QFP	-40 to 85°C -40 to 105°C -40 to 125°C
SAMPLE PACKS	PACKAGE	TEMPERATURE RANGE
KMC908AZ60ACFU	64 QFP	-40 to 85°C

* All prices are manufacturer's suggested resale for North America.

64-Lead QFP FU mmmmm



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68HC908AZ60APB/D Rev. 1