Vishay General Semiconductor

Dual Common Cathode Schottky Rectifier



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DESIGN SUPPORT TOOLS

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PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 8 A			
V _{RRM}	40 V			
I _{FSM}	250 A			
V _F	0.55 V			
T _J max.	125 °C			
Package	D ² PAK (TO-263AB)			
Circuit configuration	Common cathode			

FEATURES

- Power pack
- Guardring for overvoltage protection
- Low power loss, high efficiency
- Low forward voltage drop
- High forward surge capability
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 $^\circ\mathrm{C}$
- AEC-Q101 qualified available
 Automotive ordering code: base P/NHE3_A
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in low voltage, high frequency rectifier of switching mode power supplies, freewheeling diodes, DC/DC converters, and polarity protection application.

MECHANICAL DATA

Case: D²PAK (TO-263AB)

Molding compound meets UL 94 V-0 flammability rating Base P/NHE3_X - RoHS-compliant, AEC-Q101 qualified ("_X" denotes revision code, e.g. A, B, ...)

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102 HE3 suffix meets JESD 201 class 2 whisker test

Polarity: as marked

MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)				
PARAMETER		SYMBOL	SBLB1640CT	UNIT
Maximum repetitive peak reverse voltage		V _{RRM}	40	v
Working peak reverse voltage		V _{RWM}	28	
Maximum DC blocking voltage		V _{DC}	40	1
Maximum average forward rectified current at T_{C} = 95 °C	total device	I _{F(AV)}	16	A
	per diode		8.0	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	250	
Operating junction and storage temperature range		T _J , T _{STG}	-40 to +125	°C





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ELECTRICAL CHARACTERISTICS ($T_c = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUE	UNIT	
Maximum instantaneous forward voltage per diode	V _F ⁽¹⁾	8.0 A		0.55	V	
Maximum instantaneous reverse current at DC blocking voltage per diode	I _R ⁽²⁾	Rated V _R	T _C = 25 °C	0.5	mA	
			T _C = 100 °C	50		

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_c = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER	SYMBOL	SBLB1640CT	UNIT		
Typical thermal resistance from junction to case per diode	$R_{ extsf{ heta}JC}$	2.0	°C/W		

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-263AB	SBLB1640CTHE3_B/P ⁽¹⁾	1.35	Р	50/tube	Tube	
TO-263AB	SBLB1640CTHE3_B/I ⁽¹⁾	1.35	l	800/reel	Tape and reel	

Note

(1) AEC-Q101 qualified



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RATINGS AND CHARACTERISTICS CURVES ($T_C = 25$ °C unless otherwise noted)



Fig. 1 - Forward Current Derating Curve



Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current Per Diode



Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode



Fig. 4 - Typical Reverse Characteristics Per Diode



Fig. 5 - Typical Junction Capacitance Per Diode



Fig. 6 - Typical Transient Thermal Impedance Per Diode

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For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



Mounting Pad Layout

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